FIG. 1

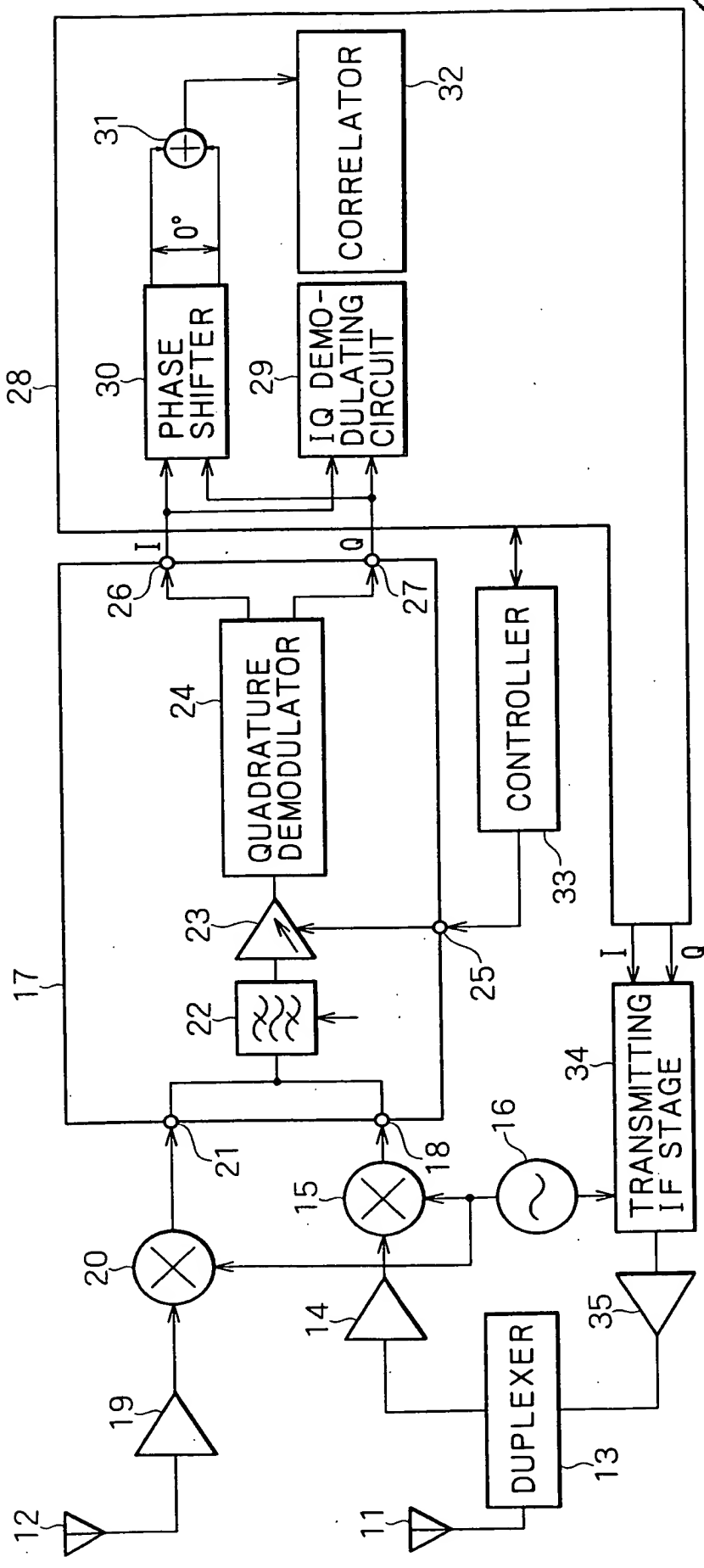

28
30
PHASE SHIFTER
0°
31
29
IQ DEMODULATING CIRCUIT
CORRELATOR
32
26
I
27
Q
24
QUADRATURE DEMODULATOR
CONTROLLER
33
23
25
17
22
I
Q
34
TRANSMITTING IF STAGE
21
18
16
15
20
14
35
19
DUPLEXER
13
12
11

FIG. 2

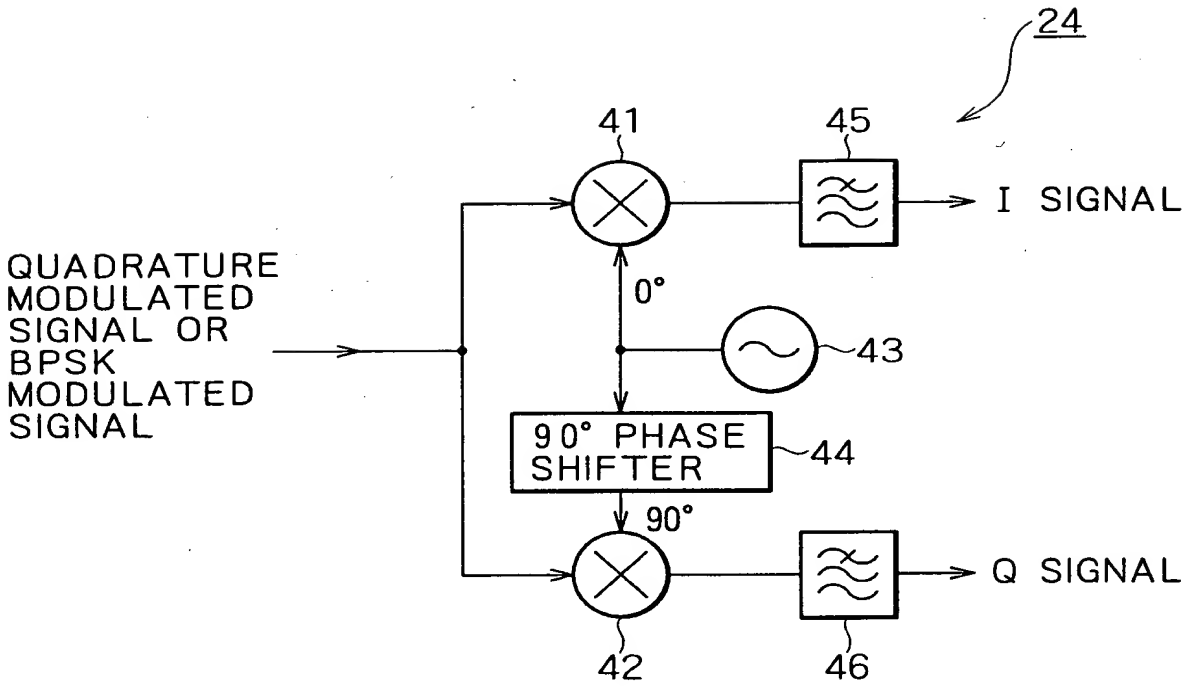


FIG. 3

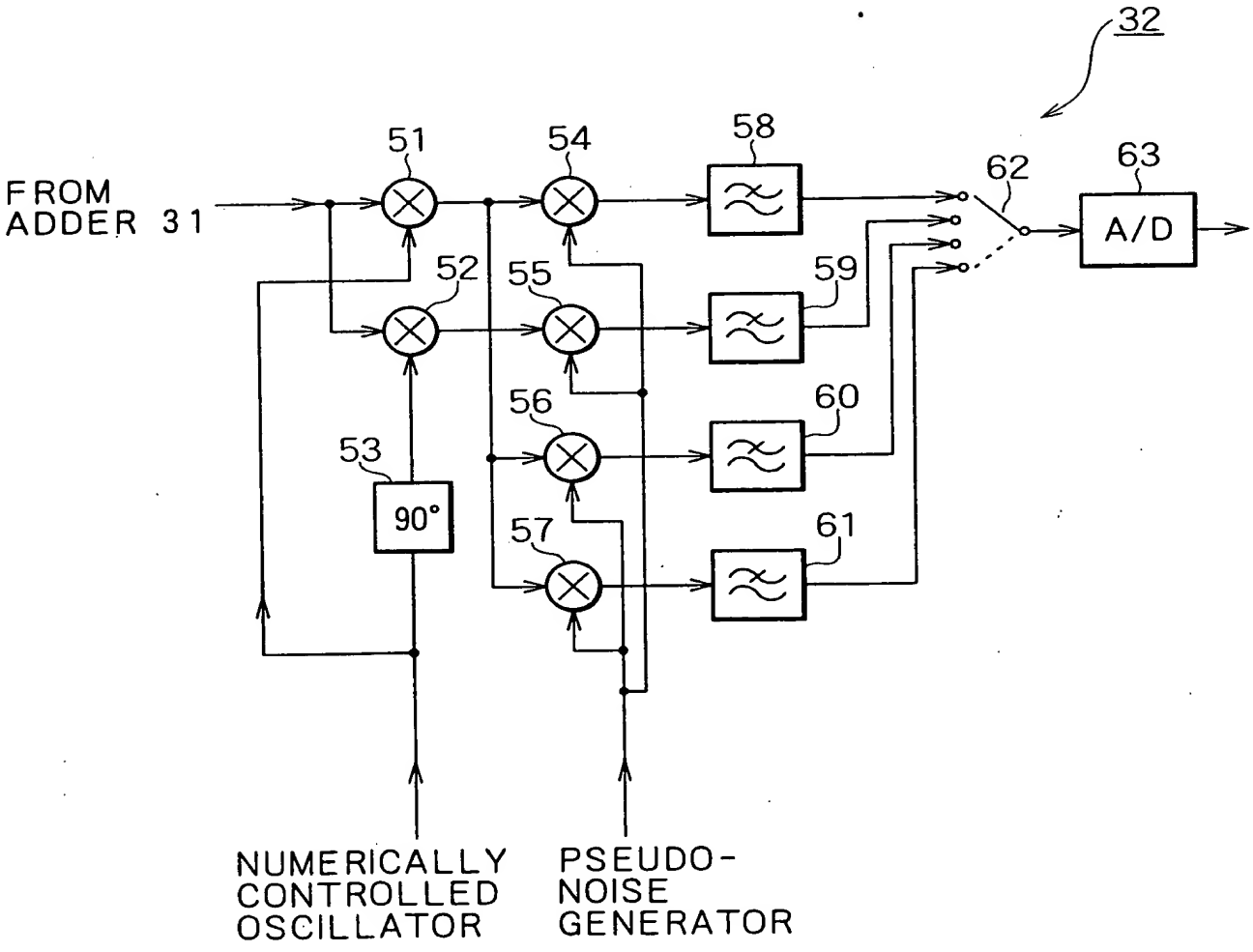

32
51
54
58
62
63
FROM ADDER 31
A/D
52
55
59
56
60
53
90°
57
61
NUMERICALLY CONTROLLED OSCILLATOR
PSEUDO-NOISE GENERATOR

FIG. 4

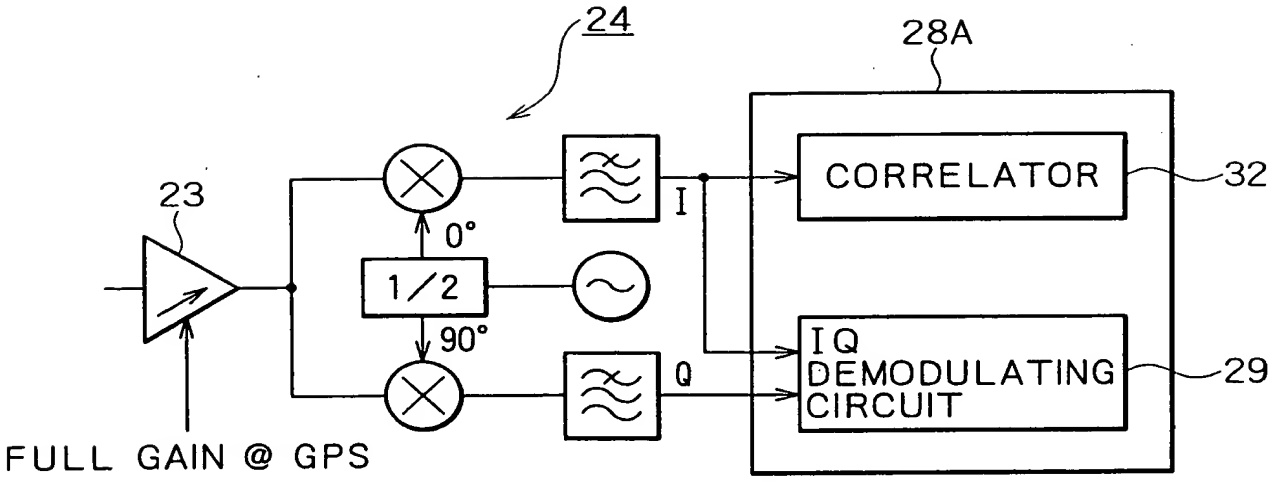
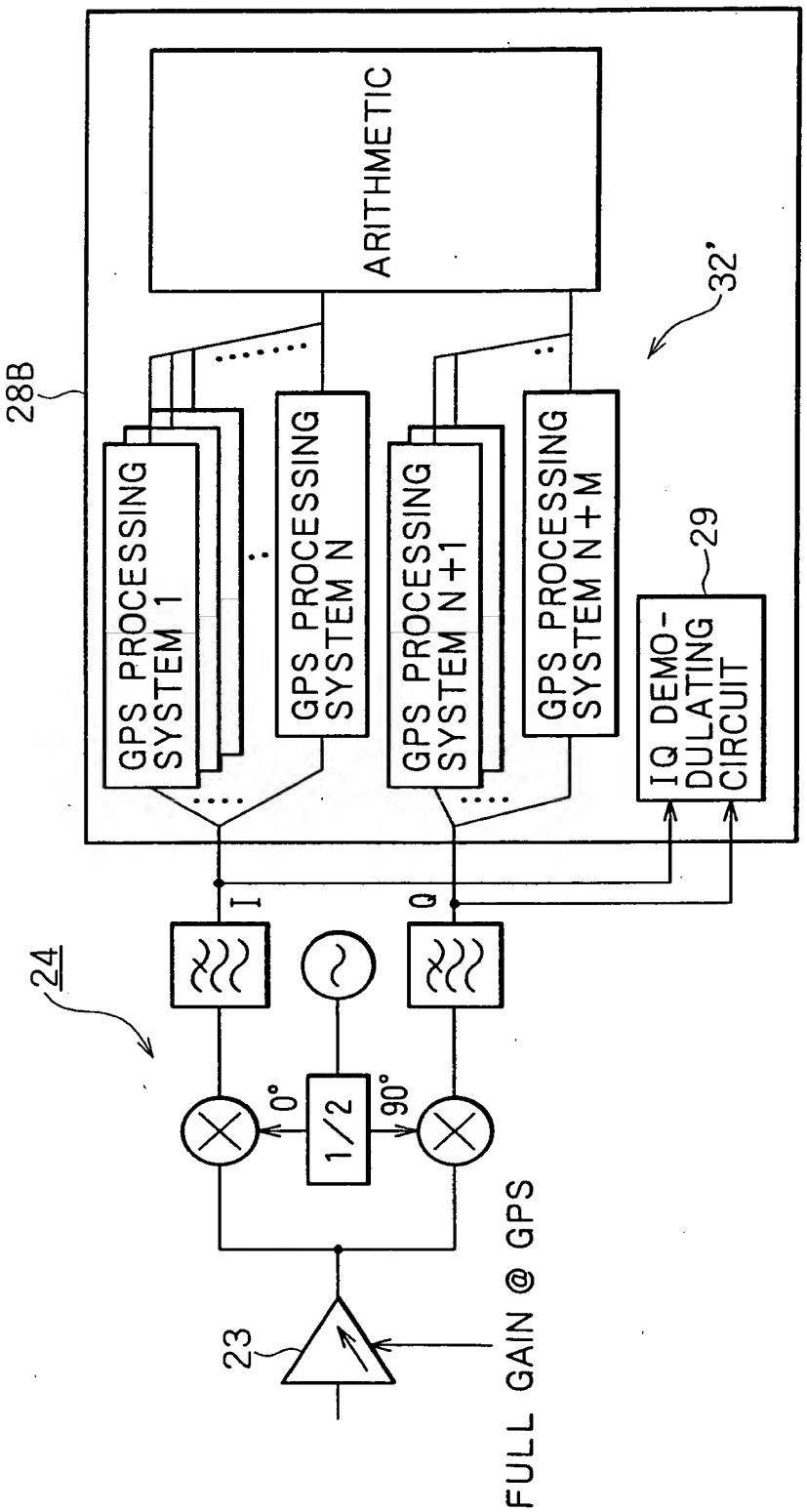

24
28A
23
0°
1/2
90°
I
Q
CORRELATOR
32
IQ
DEMODULATING
CIRCUIT
29
FULL GAIN @ GPS

FIG. 5


28B
ARITHMETIC
32'
GPS PROCESSING SYSTEM 1
GPS PROCESSING SYSTEM N
GPS PROCESSING SYSTEM N+1
GPS PROCESSING SYSTEM N+M
IQ DEMO-DULATING CIRCUIT
29
I
Q
24
0°
1/2
90°
23
FULL GAIN @ GPS